

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-6 (canceled)

7. (currently amended) A semiconductor integrated circuit comprising:

an internal circuit; and

a photodetector which comprises a first circuit

including a semiconductor element for sensitivity adjustment on a current path of the first circuit; a second circuit having whose photo-detection sensitivity is adjusted by said first circuit and having which has a photo-detecting semiconductor element on a current path of the second circuit; and a third circuit for detecting an the output node level of the second circuit, an output of the third circuit being varied according to a variation of the output node level of the second circuit when the photo-detecting semiconductor element is irradiated with light,

~~said semiconductor integrated circuit further comprising:~~

~~photodetectors for varying the output of said third circuit according to the output node level of said second circuit which is subject to current variations when said photo-detecting semiconductor element is irradiated with light,~~

wherein the photo-detection by said photodetectors is used for stopping internal actions action of the internal circuit.

8. (currently amended) The semiconductor integrated circuit according to Claim 7, wherein said photo-detecting semiconductor element is a MOS transistor ~~constituting said current path.~~

9. (currently amended) The semiconductor integrated circuit according to Claim 78,

wherein said photo-detecting semiconductor element is a diode element arranged in parallel with a part of the current path of said second circuit, and

wherein said diode element is connected in a reverse bias.

10. (currently amended) The semiconductor integrated circuit according to Claim 9, including a plurality of said diode elements,

wherein ~~a the plurality of~~ plurality of said diode elements are arranged in parallel with each other.

11. (currently amended) The semiconductor integrated circuit according to Claim 10, wherein said plurality of diode elements ~~are to be~~ ubiquitous on a semiconductor chip of the semiconductor integrated circuit.

Claims 12-17 (canceled)

18. (currently amended) ~~A~~The semiconductor integrated circuit ~~according to Claim 7, further including:~~

a power supply circuit; ~~or~~

a clock generating circuit; ~~or~~

an internal circuit; and

a plurality of photodetectors, each of the plurality of the photodetectors comprising:

a first circuit including a semiconductor element for sensitivity adjustment on a current path of the first circuit;

a second circuit having photo-detection sensitivity adjusted by said first circuit and having a photo-detecting semiconductor element on a current path of the second circuit; and

a third circuit for detecting an output node level of the second circuit, an output of the third circuit being varied according to a variation of the output node level of the second circuit when the photo-detecting semiconductor element is irradiated with light,

wherein photo-detection by each of the plurality of said photodetectors is used for stopping internal action of the internal circuit,

wherein said plurality of photodetectors are arranged in said power supply circuit and said~~er~~ clock generating circuit.

19. (currently amended) The semiconductor integrated circuit according to Claim 187, wherein ~~the~~ a current driving force of said semiconductor element for sensitivity adjustment is adjustable by a difference between a W/L value of the semiconductor element for sensitivity adjustment and a W/L value of the photo-detecting semiconductor element.

Claims 20-23 (canceled)

24. (currently amended) The semiconductor integrated circuit according to Claim 18⁷, further comprising a plurality of circuit modules₇ in ~~each of~~ which said plurality of photodetectors are arranged regularly.

Claims 25-37 (canceled)